



SD103ASDM

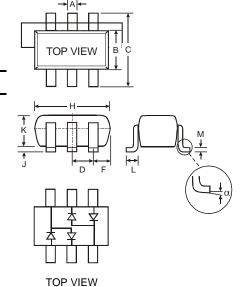
SURFACE MOUNT SCHOTTKY BARRIER DIODE ARRA

Features

- Low Forward Voltage Drop
- **Guard Ring Construction for Transient Protection**
- Fast Switching
- Low Reverse Capacitance
- Lead Free/RoHS Compliant (Note 3)
- "Green" Device, (Note 4 and 5)

Mechanical Data

- Case: SOT-26
- Case Material: Molded Plastic, "Green" Molding Compound, Note 5. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Polarity: See Diagram
- Leads: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Copper leadframe).
- Marking Code: KSU See Page 3
- Weight: 0.016 grams (approximate)



	SOT-26										
Dim	Min	Max	Тур								
Α	0.35	0.50	0.38								
В	1.50	1.70	1.60								
С	2.70	3.00	2.80								
D	_	_	0.95								
F	_	_	0.55								
Н	2.90	3.10	3.00								
J	0.013	0.10	0.05								
K	1.00	1.30	1.10								
L	0.35	0.55	0.40								
М	0.10	0.20	0.15								
α	0°	8°	_								
AII C	All Dimensions in mm										

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value		
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	40	>	
RMS Reverse Voltage	V _{R(RMS)}	28	V	
Forward Continuous Current (Note 1)	I _{FM}	350	mA	
Non-Repetitive Peak Forward Surge Current @ t ≤ 1.0s	I _{FSM}	1.5	Α	
Power Dissipation (Note 1)	P_d	225	mW	
Thermal Resistance, Junction to Ambient Air (Note 1)	$R_{\theta JA}$	444	°C/W	
Operating and Storage Temperature Range	T _j , T _{STG}	-65 to +125	°C	

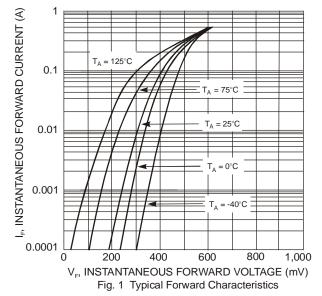
Electrical Characteristics @TA = 25°C unless otherwise specified

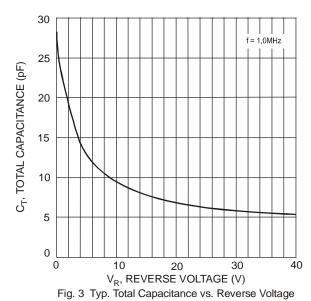
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 2)	$V_{(BR)R}$	40			V	$I_R = 100 \mu A$
				0.37		$I_F = 20mA$
Forward Voltage Drop	VF	_		0.50	V	I _F = 100mA
				0.60		$I_F = 200 \text{mA}$
Reverse Current (Note 2)	I_R	_		5.0	μΑ	$V_R = 30V$
Total Capacitance	C_{T}		50		pF	$V_R = 0V$, $f = 1.0MHz$
Reverse Recovery Time			10		20	$I_F = I_R = 200 \text{mA},$
Neverse Necovery Time	t _{rr}		10		ns	$I_F = I_R = 200 \text{mA},$ $I_{rr} = 0.1 \text{ x } I_R, R_L = 100 \Omega$

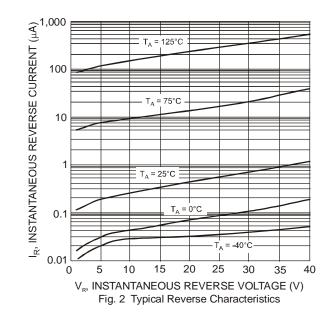
Notes: 1. Device mounted on FR-5 PCB 1.0 x 0.75 x 0.062 inch pad layout as shown on Diodes Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

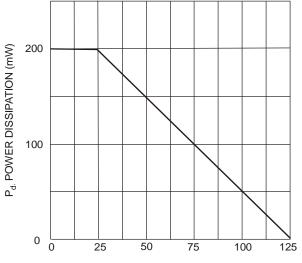
- 2. Short duration pulse test used to minimize self-heating effect.
- No purposefully added lead.
- Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
- Product manufactured with Date Code 0627 (week 27, 2006) and newer are built with Green Molding Compound. Product manufactured prior to Date Code 0627 are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants











 ${\sf T_A}, \; {\sf AMBIENT\,TEMPERATURE} \; (^\circ{\sf C})$ Fig. 4 Power Derating Curve

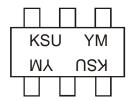


Ordering Information (Note 5 & 6)

Device	Packaging	Shipping
SD103ASDM-7-F	SOT-26	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



KSU = Product Type Marking Code YM = Date Code Marking Y =Year ex: T = 2006 M = Month ex: 9 = September

Date Code Kev

Year	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	N	Р	R	S	Т	U	V	W	Х	Υ	Z

Ī	Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
	Code	1	2	3	4	5	6	7	8	9	0	Ν	D

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